

Docket No.: 60188-145

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

FUJIWARA, Makoto, et al.

Serial No.: Continuation of

Application Serial No. 09/418,312

Group Art Unit: Not yet assigned

Filed: February 08, 2002

Examiner: Not yet assigned

For: METHOD FOR DESIGNING INTEGRATED CIRCUIT DEVICE

Handwritten: #2/16/02, #2/16/02, #2/16/02
PATENT



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 09/418,312, filed October 14, 1999, which is relied upon for an earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR 1.98(d).

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Handwritten signature of Michael E. Fogarty
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SERIAL NO.
(Cont. of Appln. Serial
No. 09/418,312)

GROUP
Not yet assigned

[illegible]

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
	08221371	08/1996	Japan				
	05325539	12/1993	Japan				
	10336211	12/1998	Japan				

DATE CONSIDERED